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ADACEL - air traffic control simulator, ATC simulator

A simulation is an approximate imitation of the operation of a process or system; the act of simulating first requires a model is developed. This model is a well-defined description of the simulated subject, and represents its key characteristics, such as its behaviour, functions and abstract or physical properties.

Simulation - Wikipedia

Our simulation solution CarMaker includes a complete model environment comprising an intelligent driver model, a detailed vehicle model and highly flexible models for roads and traffic.

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Note: Intel technologies' features and benefits depend on system configuration and may require enabled hardware, software or service activation.Performance varies ...

Application Notes - intel.com

Simulation software is based on the process of modeling a real phenomenon with a set of mathematical formulas. It is, essentially, a program that allows the user to observe an operation through simulation without actually performing that operation.

Simulation software - Wikipedia

Overview of the simulation model of the IEEE 802.15.4 protocol. The idea behind this simulation model was triggered by the need to build a very reliable model of the IEEE 802.15.4/ZigBee protocols for Wireless Sensor Networks (WSNs). According to our personal experience, we strongly believe that the current version of the WPAN implementation in the Newtwork Simulator (ns-2) simulator is not ...

open-ZB.net - IEEE 802.15.4/ZigBee OPNET Simulation Model

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User Manual iGO primo Navigation software for the [DEVICE] UK English October 2012, ver. 1.0

User Manual - Dynavin

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(#VIS19266) What is the minimum number of runs for the simulation experiment to be considered statistically significant? There is no general rule to this as it depends on the Vissim application:

PTV Vissim FAQs - Transportation planning, traffic

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Allison Electric Drives EP System - Gary Stadler

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External Memory Interface Handbook Volume 2: Design Guidelines

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Application of Statistical Tools for Data Analysis and

In this paper we show how to create a UVM testbench with interface connections that universally work in any design simulation context. A harness is a common solution for encapsulating interfaces, binding them to the DUT, and publishing virtual interface assignments.

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